

FPBPXX2GS(L)-XX

2.5G SFP BIDI Transceiver Module

Features

- ✧ Operating data rate up to 2.125 / 2.5Gbps
- ✧ Tx1310/Rx1550 & Tx1550/Rx1310;
Tx1490/Rx1550 & Tx1550/Rx1490
- ✧ APD Distance up to 80KM
- ✧ Single 3.3V Power supply and TTL Logic Interface
- ✧ Simplex SC/ LC Connector Interface
- ✧ Hot Pluggable, Metal enclosure, for Lower EMI
- ✧ Operating Case Temperature:
Standard: 0°C~+70°C ; Industrial: -40°C~+85°C
- ✧ Compliant with MSA SFP Specification
- ✧ Digital Diagnostic Monitor Function Compatible with SFF-8472 and DDM optional
- ✧ ROHS6 Compliant



Applications

- ✧ Fiber Channel Infrastructure
- ✧ STM-16 Optical interface
- ✧ ATM Switches and Routers
- ✧ SDH/SONET Switch Infrastructure

Product Description

The FPBPXX2GL(S)-XX series single mode transceivers is small form factor pluggable module for bi-directional serial optical data communications such as STM-16, OC-48, Fiber Channel, It is SFP 20-pin connector to allow hot plug capability. This module is designed for single mode fiber and operates at a nominal wavelength of 1310nm (1490,1550nm).

The transmitter section uses a multiple quantum well laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an integrated

InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

The FPBPXX2GL-XXDI series are designed to be compliant with SFF-8472 SFP Multi-source Agreement (MSA) and Industry working temperature.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit
Storage Temperature	Ts	-40		85	°C
Supply Voltage	VCC	-0.5		3.6	V

Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit
Ambient Operating Temperature	T _A	0		70	°C
Supply Voltage	VCC	3.15	3.3	3.45	V
Data Rate	OC-48 / STM-16		2.5	2.67	Gbps
	2 Fiber Channel		2.125		
Total Supply Current	I _{CC}			300	mA
Surge Current	I _{surge}			+30	mA

Performance Specifications - Electrical

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
TRANSMITTER						
CML/PECL Inputs (Differential)	V _{in}	400		1800	mVpp	AC coupled inputs
Input Impedance (Differential)	Z _{in}	90	100	110	ohms	R _{in} > 100 kohms @ DC
Tx_DISABLE Input Voltage - High		2		3.45	V	
Tx_DISABLE Input Voltage - Low		0		0.8	V	
Tx_FAULT Output Voltage -- High		V _{cc} -0.5		V _{cc} +0.3	V	I _o = 400μA; Host V _{cc}
Tx_FAULT Output Voltage		0		0.5	V	I _o = -4.0mA

-- Low						
TX_Disable Assert Time	t _{off}			10	us	
TX_Disable Negate Time	t _{off}			1	us	
RECEIVER						
CML Outputs (Differential)	V _{out}	370		1800	mVpp	AC coupled outputs
Output Impedance (Differential)	Z _{out}	90	100	110	ohms	
Rx_LOS Output Voltage - High		V _{cc} -0.5		V _{cc} +0.3	V	I _o = 400μA; Host V _{cc}
Rx_LOS Output Voltage - Low		0		0.8	V	I _o = -4.0mA

Performance Specifications- Optical

Parameter		Symbol	Min.	Typical	Max.	Unit
9μm Core Diameter SMF	FPBP352GL(S)-5XX	FP		5		Km
	FPBP532GL(S)-5XX	FP				
	FPBP352GL(S)-20XX	DFB		20		
	FPBP532GL(S)-20XX			40		
	FPBP352GL(S)-40XX					
	FPBP532GL(S)-40XX					
	FPBP452GL(S)-80XX			80		
	FPBP542GL(S)-80XX					
Data Rate				2.5/2.125		Gbps
Transmitter						
Centre Wavelength		λ _c	1260	1310	1360	nm
			1420	1490	1520	
			1500	1550	1580	
Spectral Width (RMS)		λ			3	nm
Average Output Power	FPBP352GL(S)-5XX	P _{out}	-10		-5	dBm
	FPBP532GL(S)-5XX		-10		-5	
	FPBP352GL(S)-20XX		-8		-2	
	FPBP532GL(S)-20XX		-8		-2	
	FPBP352GL(S)-40XX		-2		3	
	FPBP532GL(S)-40XX		-2		3	
	FPBP452GL(S)-80XX		0		5	
	FPBP542GL(S)-80XX		0		5	
Extinction Ratio		EX	8.5			dB

Output Optical Eye		ITU-T G.957.Compliant				
Receiver						
Centre Wavelength		λ_c	1260		1620	nm
Receiver Sensitivity	FPBP352GL(S)-5XX	PIN			-18	dBm
	FPBP532GL(S)-5XX				-18	
	FPBP352GL(S)-20XX				-19	
	FPBP532GL(S)-20XX				-19	
	FPBP352GL(S)-40XX				-21	
	FPBP532GL(S)-40XX				-21	
	FPBP452GL(S)-80XX	APD			-29	
	FPBP542GL(S)-80XX				-29	
Receiver Overload		P_{max} -PIN	-3		0	dBm
		P_{max} -APD	-9			
Rise/Fall Time		Tr/tf			2.2	ns
LOS De-Assert	PIN	LOS_D			-22	dBm
	APD				-33	
LOS Assert	PIN	LOS_A	-35			dBm
	APD		-40			

SFP Transceiver Electrical Pad Layout

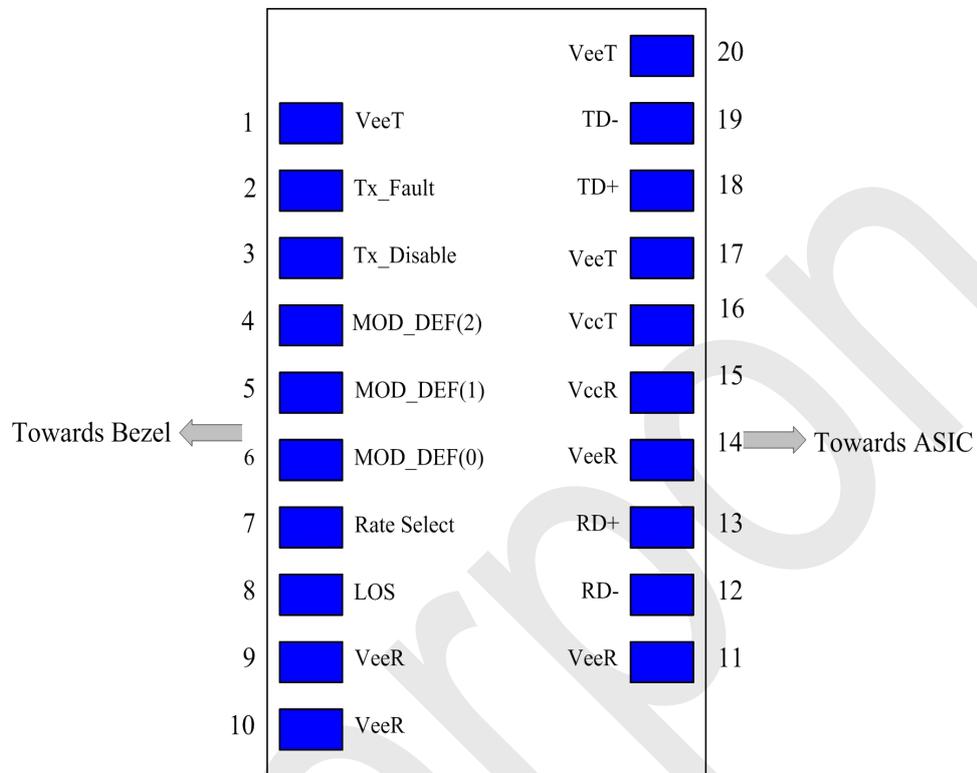


Figure 1 Transceiver pin descriptions

Pin Function Definitions

Pin Number	Name	FUNCTION	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	
2	TX Fault	Transmitter Fault Indication	3	Note 1
3	TX Disable	Transmitter Disable	3	Note 2, Module disables on high or open
4	MOD-DEF2	Module Definition 2	3	Note 3, Data line for Serial ID.
5	MOD-DEF1	Module Definition 1	3	Note 3, Clock line for Serial ID.
6	MOD-DEF0	Module Definition 0	3	Note 3, Grounded within the module.
7	Rate Select	Not Connect	3	Function not available
8	LOS	Loss of Signal	3	Note 4
9	VeeR	Receiver Ground	1	Note 5

10	VeeR	Receiver Ground	1	Note 5
11	VeeR	Receiver Ground	1	Note 5
12	RD-	Inv. Received Data Out	3	Note 6
13	RD+	Received Data Out	3	Note 7
14	VeeR	Receiver Ground	1	Note 5
15	VccR	Receiver Power	2	3.3 ± 5%, Note 7
16	VccT	Transmitter Power	2	3.3 ± 5%, Note 7
17	VeeT	Transmitter Ground	1	Note 5
18	TD+	Transmit Data In	3	Note 8
19	TD-	Inv. Transmit Data In	3	Note 8
20	VeeT	Transmitter Ground	1	Note 5

Notes:

1) TX Fault is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

2) TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7 – 10 K Ω resistor. Its states are:

- ✧ Low (0 – 0.8V): Transmitter on
- ✧ (>0.8, < 2.0V): Undefined
- ✧ High (2.0 – 3.465V): Transmitter Disabled
- ✧ Open: Transmitter Disabled

3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7K – 10KΩresistor on the host board. The pull-up voltage shall be VccT or VccR (see Section IV for further details). Mod-Def 0 is grounded by the module to indicate that the module is present Mod-Def 1 is the clock line of two wire serial interface for serial ID Mod-Def 2 is the data line of two wire serial interface for serial ID

4) LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

5) VeeR and VeeT may be internally connected within the SFP module.

6) RD-/+: These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 370 and 2000 mV differential (185 –1000 mV single ended) when properly terminated.

7) VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V ±5% at the SFP connector pin. Maximum supply current is 300mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 ohm should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30mA greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.

8) TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 500 – 2400 mV (250 – 1200mV single-ended), though it is recommended that values between 500 and 1200 mV differential (250 – 600mV single-ended) be used for best EMI performance.

Digital Diagnostic Memory Map

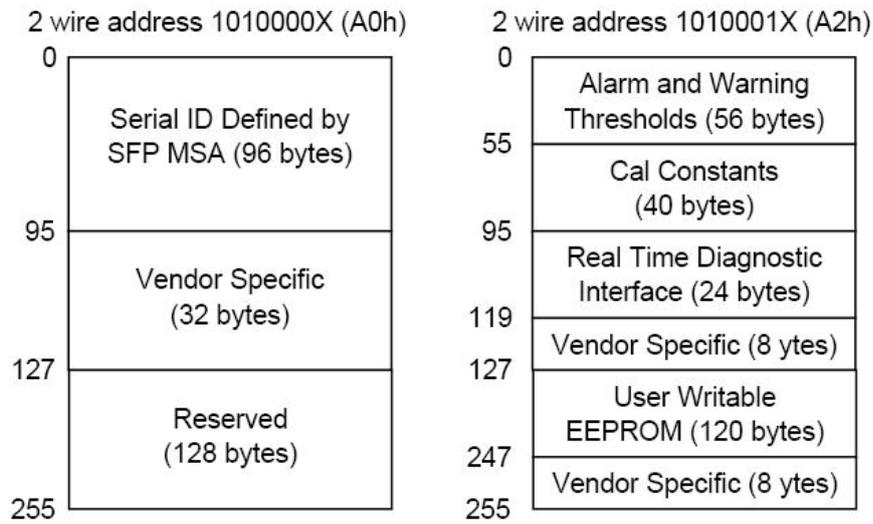


Figure 2 Digital Diagnostic Memory Map

Recommend Circuit Schematic

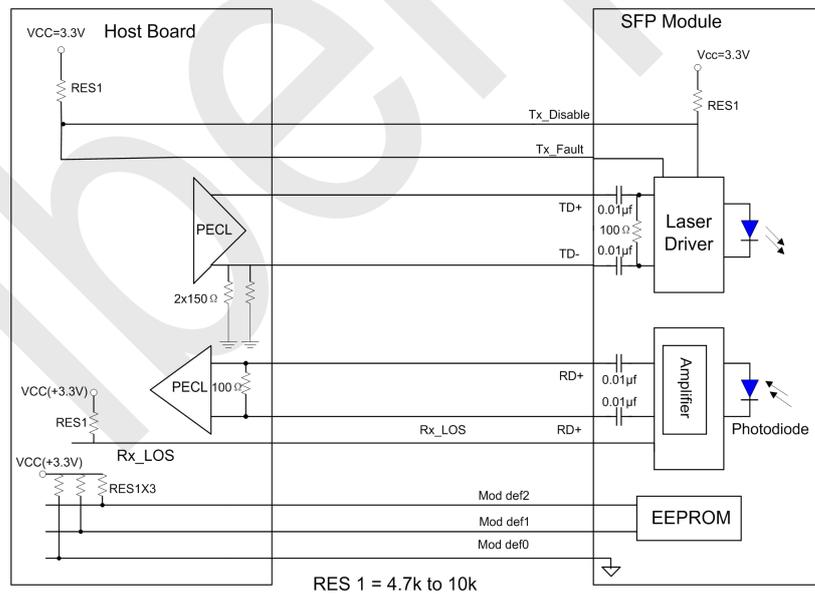
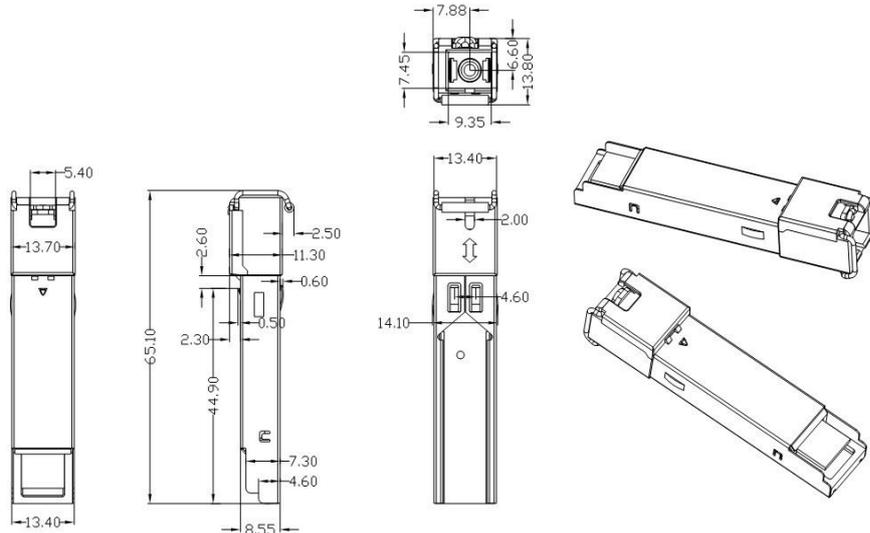


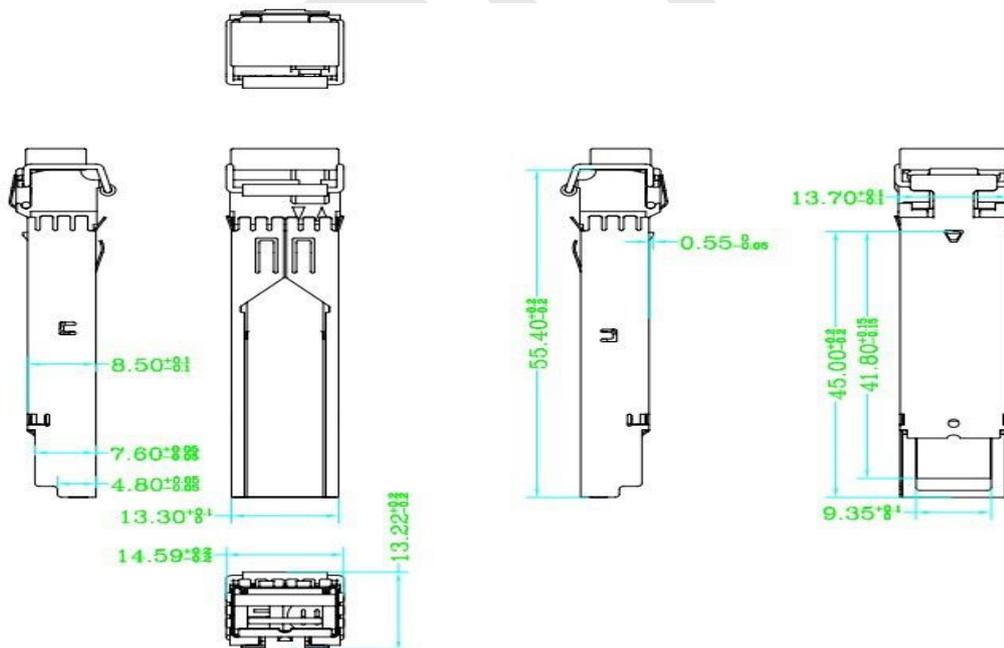
Figure 3 Recommend Circuit Schematic

Mechanical Specifications

SC Receptacle



LC Receptacle



Ordering information

Product Number	Data Rate	Laser	Receiver	Distance	Interface	DDM	Temp.
FPBP352GL(S)-5D	2.5Gbps	FP	PIN-TIA	5Km	LC / SC	YES	C
FPBP532GL(S)-5D	2.5Gbps	FP	PIN-TIA	5Km	LC / SC	YES	C
FPBP352GL(S)-20D	2.5Gbps	DFB	PIN-TIA	20Km	LC / SC	YES	C
FPBP532GL(S)-20D	2.5Gbps	DFB	PIN-TIA	20Km	LC / SC	YES	C
FPBP352GL(S)-40D	2.5Gbps	DFB	PIN-TIA	40Km	LC / SC	YES	C
FPBP532GL(S)-40D	2.5Gbps	DFB	PIN-TIA	40Km	LC / SC	YES	C
FPBP451GL(S)-80DI	2.5Gbps	DFB	APD-TIA	80Km	LC / SC	YES	I
FPBP541GL(S)-80DI	2.5Gbps	DFB	APD-TIA	80Km	LC / SC	YES	I

***I--- Industrial operating temperature**

***C--- Commercial Temperature**

***D--- DDM / DOM**

Notice

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